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## **POWER OF ATTORNEY CORRESPONDENCE ADDRESS** INDICATION FORM

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POWER OF ATTORNEY and RESPONDENCE ADDRESS INDICATION FORM	Application Number	10/650,296
	Filing Date	August 27, 2003
	First Named Inventor	Tetelbaum, Alexander
	Title	Method of Clock Driven Cell
	Art Unit	2825
	Examiner Name	Siek, Vuthe
	Attorney Docket Number	03-0861

I hereby revoke all previous powers of attorney given in the above-identified application.						
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Assignee of record of the entire interest. See 37 CFR 3.71.						
Statement under 37 CFR 3.73(b) is enclosed. (Form PTO/SB/96)						
\$IGNATURE of Applicant or Assignee of Record						
Signature / M K G	Date 6 MAR 06					
Name Timothy R. Croll	Telephone (408) 433-7625					
Title and Company Senior Corporate Counsel, IP, LSI Logic Corporation						
NOTE: Signatures of all the inventors or assigness of record of the entire interest or their representative(s) are required. Submit multiple forms if more than one signature is required, see below.						
*Total of 1 forms are submitted.						

This collection of information is required by 37 CFR 1.31, 1.32 and 1.33. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to take 3 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandris, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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PTO/S8/96 (12-05)
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	STATEMENT UNDER 37 CFR 3.73(b)			
Applicant	Patent Owner: LSI Logic Corporation			
Application	n No./Patent No./Control No.: 10/650	Filed/Issue Date:	August 27, 2003	
Entitled: Method of Clock Driven Cell Placament and Clock Tree Synthesis for Integrated Circuit Design				
LSI Logi	c Corporation	, a Corporation	de la company de la la company de la company	
states the	(Nama of Assignee)	(Type of Assignée: corpo	pration, partnership, university, government agency, etc.)	
1. The	assignee of the entire right, title, and	interest; or		
2. an assignce of less than the entire right, title and interest (The extent (by percentage) of its ownership interest is				
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A. An assignment from the inventor(s) of the patent application/patent identified above. The assignment was recorded in the United States Patent and Trademark Office at Reel <u>014448</u> , Frame <u>0776</u> , or a true copy of the original assignment is attached.				
OR  B. A chain of title from the inventor(s), of the patent application/patent identified above, to the current assignee as follows:				
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	3. From:	To:		
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Additional documents in the chain of title are listed on a supplemental sheet.				
	e was, or concurrently is being, sui	bmitted for recordation pursuant to of the original assignment documen	of title from the original owner to the to 37 CFR 3.11. It(s)) must be submitted to Assignment the records of the USPTO. See MPEP	
The und	ersigned (whose title is supplied below	w) is authorized to act on behalf of th	ne assignee.	
-	Signatu	re	Date	
	Timothy B.	Croll	(408) 433-7625	
	Printed or Type	d Name	Telephone Number	
	Senior Corporate Title	Counsel IP		
ŀ	Title			

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